

CLAIM LISTING

This listing of claims will replace all prior versions, and listings of claims in the application:

IN THE CLAIMS

1. (Currently Amended) A transmitter circuit for transmitting a test signal from a programmable input/output pin of a programmable logic device, the transmitter circuit comprising an amplifier having an amplifier control terminal, a first supply terminal connected to the programmable input/output pin, and a first amplifier output terminal.
2. (Currently Amended) A transmitter circuit for transmitting a test signal from a programmable input/output pin of a programmable logic device, the transmitter circuit comprising an amplifier having an amplifier control terminal, a first supply terminal connected to the input/output pin, and a first amplifier output terminal, ~~the~~ The transmitter circuit of claim 1, further comprising a second amplifier having a second amplifier control terminal, a second supply terminal connected to a power supply line of the programmable logic device, and a second amplifier output terminal.
3. (Original) The transmitter circuit of claim 2, further comprising:
 - a. a first transistor having a first current-handling terminal connected to the input/output pin, a second current-handling terminal, and a first control terminal connected to the first amplifier output terminal; and
 - b. a second transistor having a third current-handling terminal connected to the input/output pin, a fourth

current-handling terminal connected to the second current-handling terminal, and a second control terminal connected to the second amplifier output terminal.

4. (Currently Amended) The transmitter circuit of claim 1, further comprising a transistor having a first current-handling terminal connected to the programmable input/output pin, a second current-handling terminal, and a control terminal connected to the first amplifier output terminal.